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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Zandra Smith

Patent No.: 7,439,158

Group Art Unit: 2822

Issue Date: October 21, 2008

Docket No: 2003-0427.00/US

Title: STRAINED SEMICONDUCTOR BY FULL WAFER BONDING

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch

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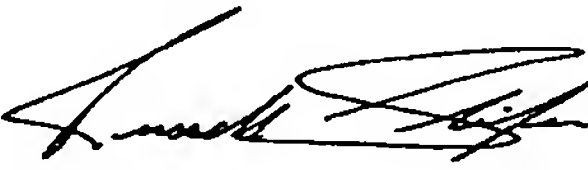
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- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
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Customer Number: 26809

Respectfully submitted,

Date: December 2, 2008

By: 
Russ Slifer
Reg. No. 39,838

**Certificate
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Christine Hartness

Christine Hartness

Name

Signature

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Patentee: Leonard Forbes et al.

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REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 1.20(a), the examiner is authorized to charge the Certificate of Correction fee of \$100.00 to the Deposit Account No. 13-3092.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

Date: December 2, 2008

By: 

Russ Slifer

Reg. No. 39,838

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,439,158

Page (1) of 1

DATED : October 21, 2008

INVENTOR(S) : Forbes et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 19, line 39, in Claim 8, delete "clam" and insert - - claim - -, therefor.

In column 23, line 58, in Claim 52, delete "column" and insert - - column - -, therefor.

In column 24, line 61, in Claim 56, delete "wafer." and insert - - wafer; - -, therefor.

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